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APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/976,052	10/15/2001	Hirokatsu Hayashi	HITA.0110	1675

TITLE OF INVENTION: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	\$1330	\$300	\$1630	06/24/2004	
EXAMINER		ART UNIT	CLASS - SUBCLASS	1		

NGUYEN, MINH T 2816 327-333000

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1. Reed Smith LLP

2. Stanley P. Fisher, Esq.

3. Juan Carlos A. Marquez, Esq.

ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

RENESAS TECHNOLOGY CORP. HITACHI ULSI SYSTEMS CO., LTD.

Tokyo, Japan Tokyo, Janan

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PTOL-85 (Rev.07-01) Approved for use through 01/31/2004. OMB 0651-0033

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